CAD system for self-timed electronic circuits RONIS

Abstract

A CAD system for speed-independent (SI) circuits is described in the report. The system gives means for development of specific aspects of electronic circuits of this class at a functional level. VHDL is accepted as a description language in the system, that provides easy interface to CAD systems of a general purpose for final designing and manufacturing of products. A library subsystem and subsystems of the analysis and synthesis of SI-circuits are developed in the CAD system. In actual version of the system the base library of crystals MIET 5503 and 5508 is realized. Programs of analysis and synthesis of ST-circuits are created and successfully applied.